

AMENDMENTS TO THE CLAIMS

Please amend claims 1-3 as shown in the complete list of claims presented below:

1. (Currently Amended) A method for preventing data corruption in a floppy diskette controller ~~Floppy Diskette Controller~~, applied to in a computer system, the computer system having[[;]] a central processing unit[[;]], a system interrupt clock[[;]], a floppy diskette[[;]], a floppy diskette controller for controlling the data transfer to the floppy diskette[[;]], peripherals associated with the floppy diskette controller for providing a direct memory access (DMA) request (DREQ) and a DMA acknowledgement (DACK), the DREQ being issued when data transfer is requested by the computer system and the DACK being issued when data transfer is permitted[[;]]. the method comprising the steps of:

determining for all of the data transferred to the floppy diskette, if a requested computer system operation accesses the data from ~~a FDG~~ the floppy diskette controller;;

measuring the time for the DMA request (DREG) (DREQ) from the issue to the removal thereof; and

signaling an error from the computer system if the measured time exceeds a specific value.

2. (Currently Amended) The method of Claim 1, further comprising the steps of:

pre-hooking an interpose service routine to an interrupt vector intercepted by the system interrupt clock;

increasing the an interrupt rate provided by the system interrupt clock, wherein the measured said measuring the time is performed through the interpose service routine for every interrupt; and

recovering the system interrupt clock to interrupt normally after the floppy diskette data transfer is completed and unhooking the interrupt vector.

3. (Currently Amended) A method for preventing data corruption in a floppy diskette controller ~~Floppy Diskette Controller~~, applied to in a computer system, the computer system having[[;]] a central processing unit[[;]], a system interrupt clock[[;]], a floppy diskette[[;]], a floppy diskette controller for controlling the data transfer to the floppy

diskette[[;]], peripherals associated with the floppy diskette controller for providing a direct memory access (DMA) request (DREQ) and a DMA acknowledgement (DACK), the DREQ being issued when data transfer is requested and the DACK being issued when data transfer is permitted[[;]], the method comprising the steps of:

determining for all of the data transferred to the floppy diskette, if a requested computer system operation accesses the data from a FDC the floppy diskette controller;

programming the system interrupt clock to increase the an interrupt rate provided by the system interrupt clock, wherein the existence of the DMA request (DREQ) is detected for every interrupt issued by the system interrupt clock;

calling the a floppy diskette service routine of the computer system so as to access the data from the floppy diskette;

measuring the time for the DMA request (DREQ) (DREQ) from the issue to the removal and recording the a maximum time;

signaling an error from the computer system if the measured time exceeds a specific value; and

reprogramming the system interrupt clock to recover the interrupt at a normal rate.